

L Number	Hits	Search Text	DB	Time stamp
1	628122	((integrated adj\$1 circuit\$1) or IC) and (CPU or MPU or processor\$1 or microprocessor\$1 or controller\$1 or microcontroller\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 10:46
2	267208	((integrated adj\$1 circuit\$1) or IC) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 10:48
3	262379	(integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 10:49
4	2566	((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 10:53
5	876	((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 10:58
6	732	((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)) and ((circuit\$1 or block\$1) same (initial\$4 or enabl\$4 or permit\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 11:01
7	148	((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)) and ((circuit\$1 or block\$1) same (initial\$4 or enabl\$4 or permit\$4))) and (ASIC or "application specific integrated circuit" or one-chip or mono\$)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 11:07
8	70	((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)) and ((circuit\$1 or block\$1) same (initial\$4 or enabl\$4 or permit\$4))) and (ASIC or "application specific integrated circuit" or one-chip or mono\$)) and (print\$4 or ink\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 11:17
9	64	((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)) and ((circuit\$1 or block\$1) same (initial\$4 or enabl\$4 or permit\$4))) and (ASIC or "application specific integrated circuit" or one-chip or mono\$)) and (print\$4 or ink\$4)) and ((time or period) same (pass\$4 or elaps\$))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 11:14

10	106	((((((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)) and ((circuit\$1 or block\$1) same (initial\$4 or enabl\$4 or permit\$4))) and (ASIC or "application specific integrated circuit" or one-chip or mono\$)) and (print\$4 or ink\$4 or record\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 11:18
11	106	((((((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)) and ((circuit\$1 or block\$1) same (initial\$4 or enabl\$4 or permit\$4))) and (ASIC or "application specific integrated circuit" or one-chip or mono\$)) and (print\$4 or ink\$4 or record\$4)) and driv\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 11:21
13	0	((((((((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)) and ((circuit\$1 or block\$1) same (initial\$4 or enabl\$4 or permit\$4))) and (ASIC or "application specific integrated circuit" or one-chip or mono\$)) and (print\$4 or ink\$4 or record\$4)) and driv\$4) and ((complet\$4 or finish\$4 or success\$\$ or fail\$4) with initial\$\$) and (((complet\$4 or finish\$4 or success\$\$ or fail\$4) with initialz\$4) same (enabl\$4 or permit\$4 or allow\$4 or grant\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 11:30
12	67	((((((((integrated adj\$1 circuit\$1) and (CPU or MPU or microprocessor\$1 or microcontroller\$1)) and ((initialz\$4 or reset\$4 or synchroniz\$4) same (power-on or (power with on)))) and ((complet\$4 or finish\$4 or success\$3 or fail\$4) same initial\$4)) and ((circuit\$1 or block\$1) same (initial\$4 or enabl\$4 or permit\$4))) and (ASIC or "application specific integrated circuit" or one-chip or mono\$)) and (print\$4 or ink\$4 or record\$4)) and driv\$4) and ((complet\$4 or finish\$4 or success\$\$ or fail\$4) with initial\$\$)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/06/27 11:30